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Fig. 1

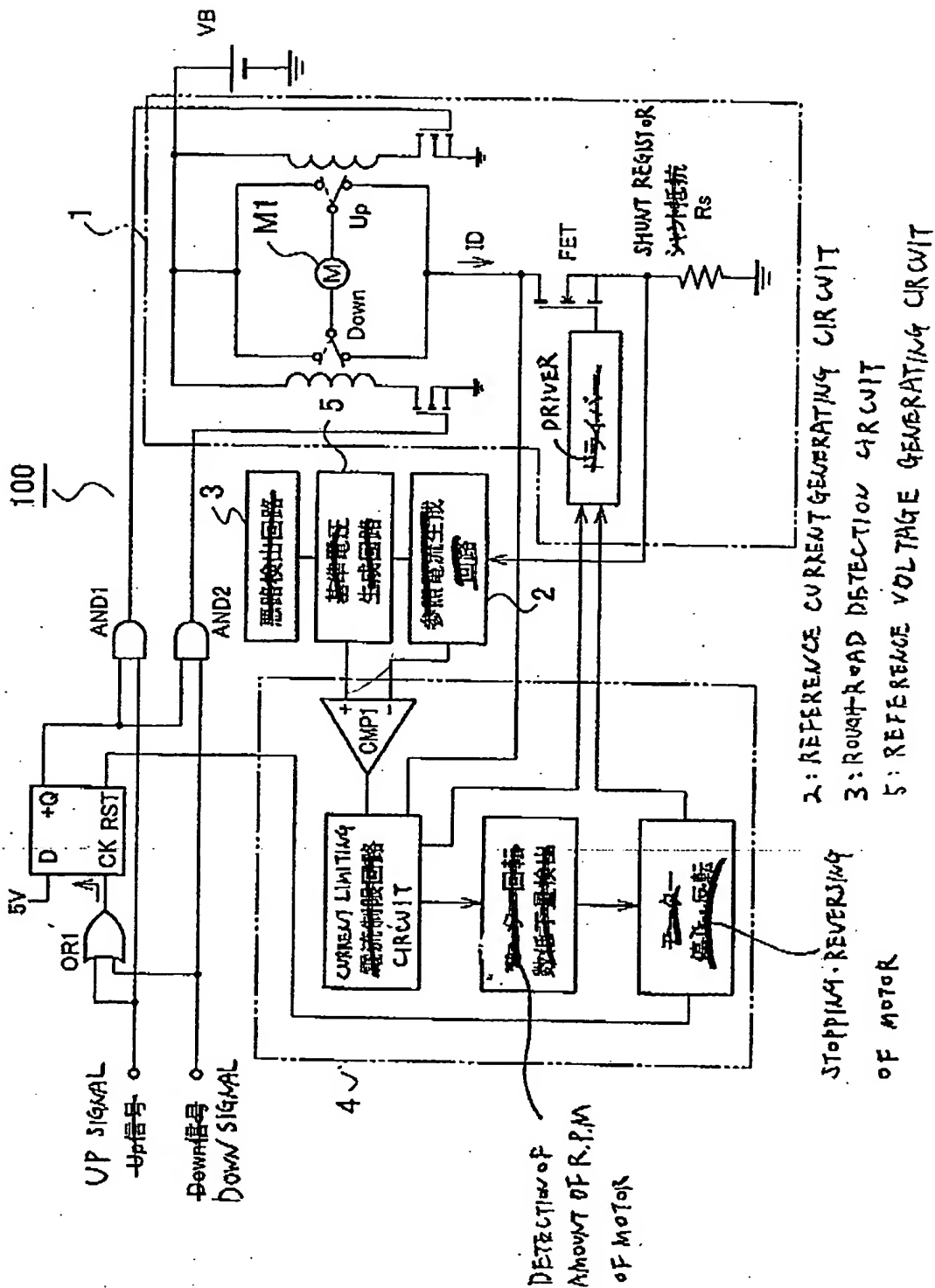


Fig. 2

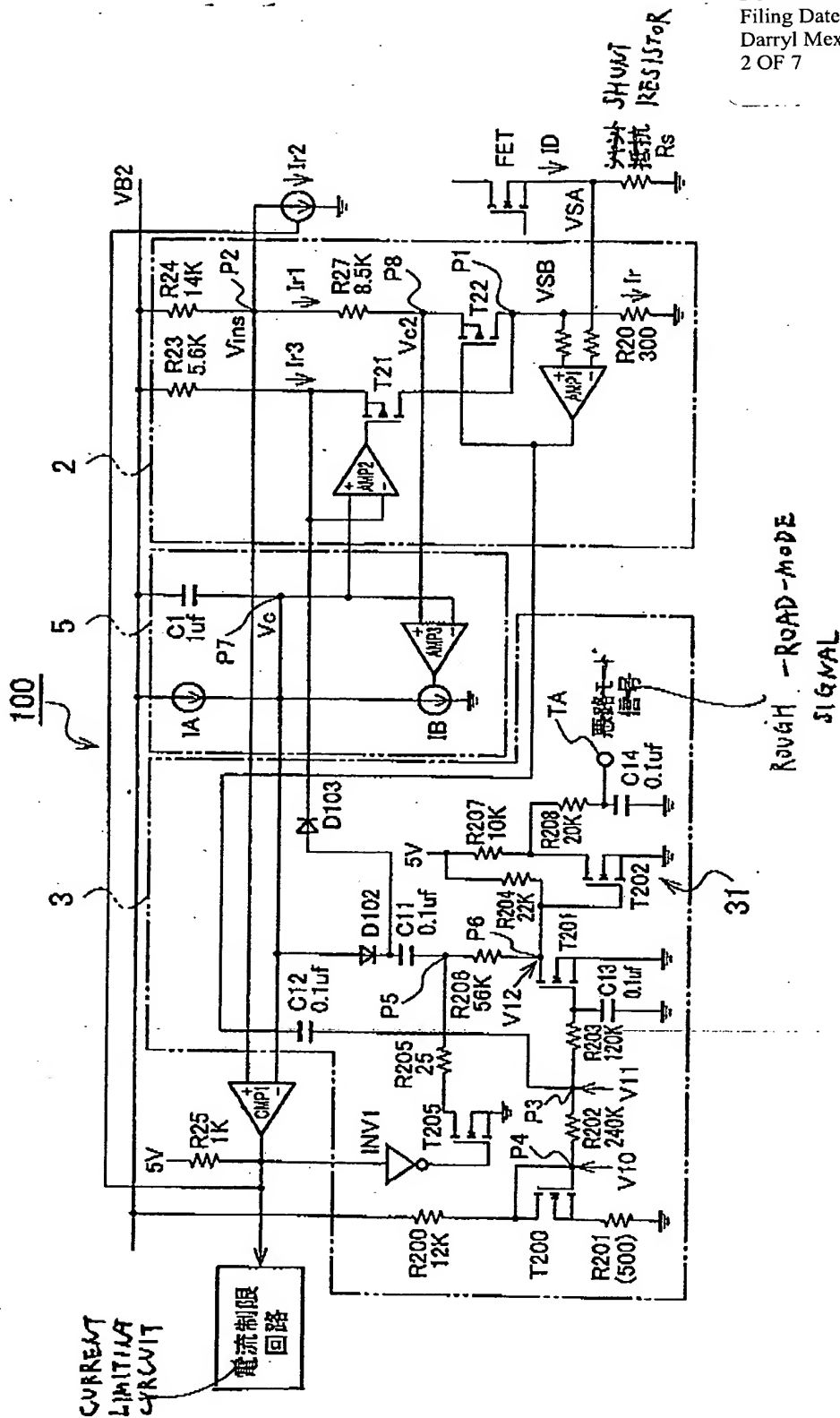


Fig. 3A

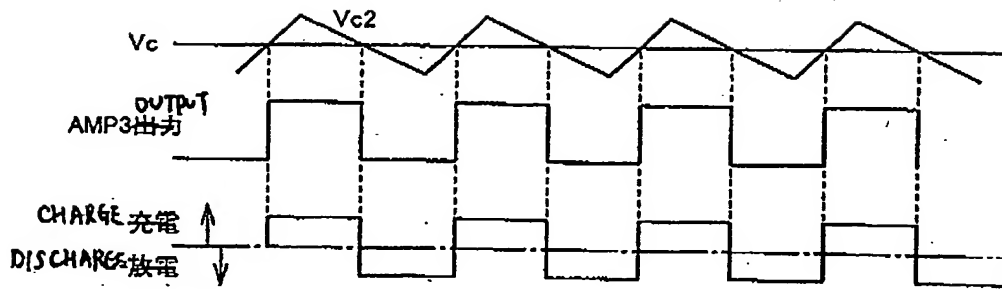


Fig. 3B

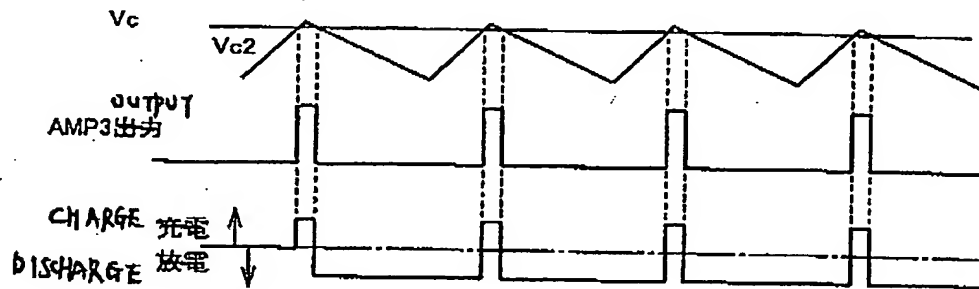


Fig. 3C

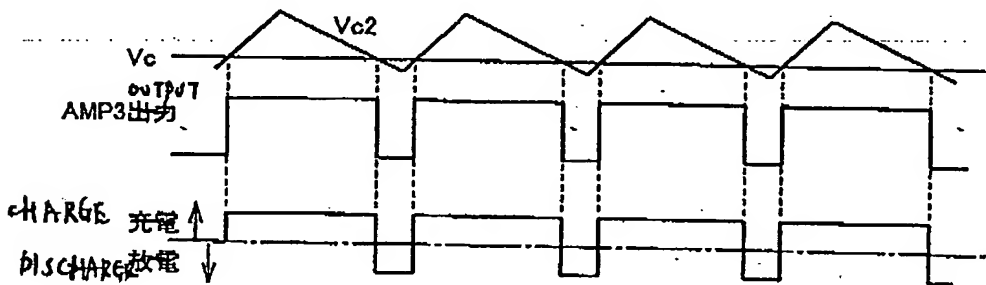


Fig. 4

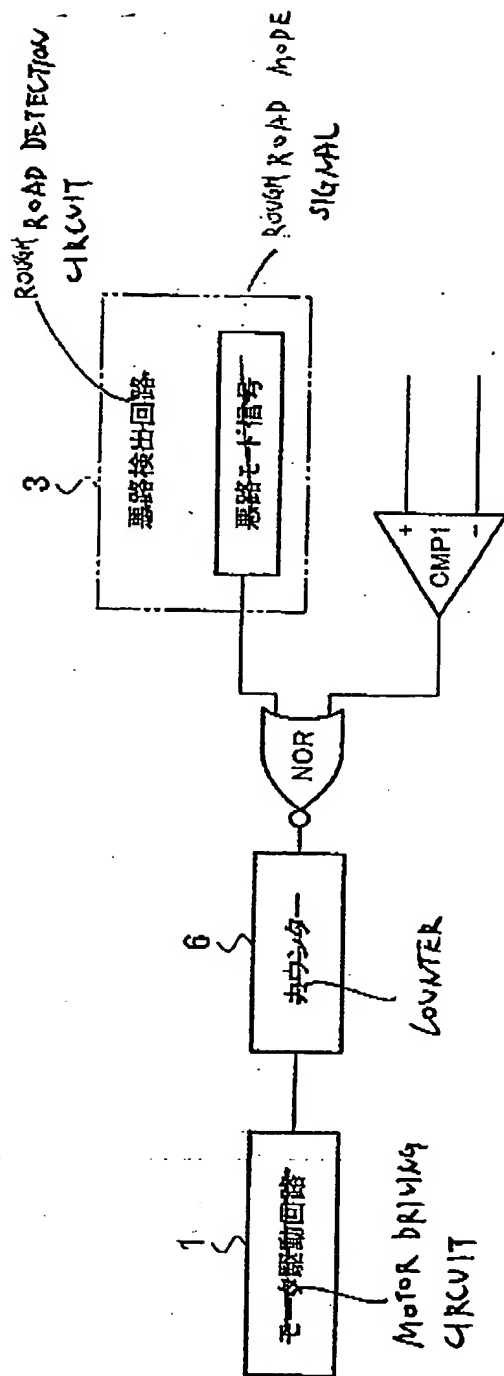


Fig. 5

ROUGH ROAD MODE

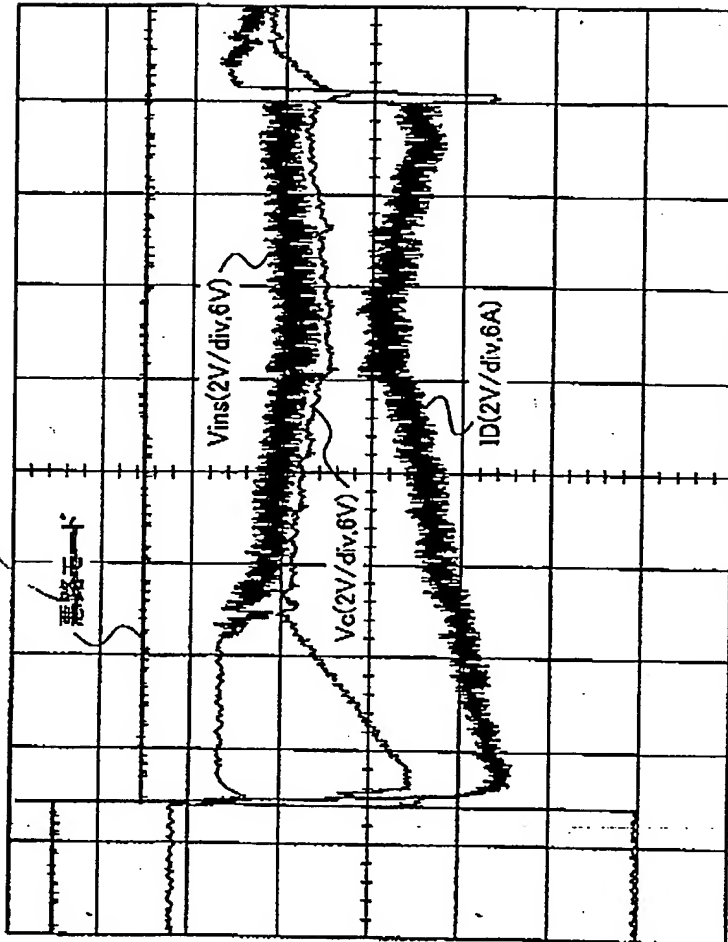
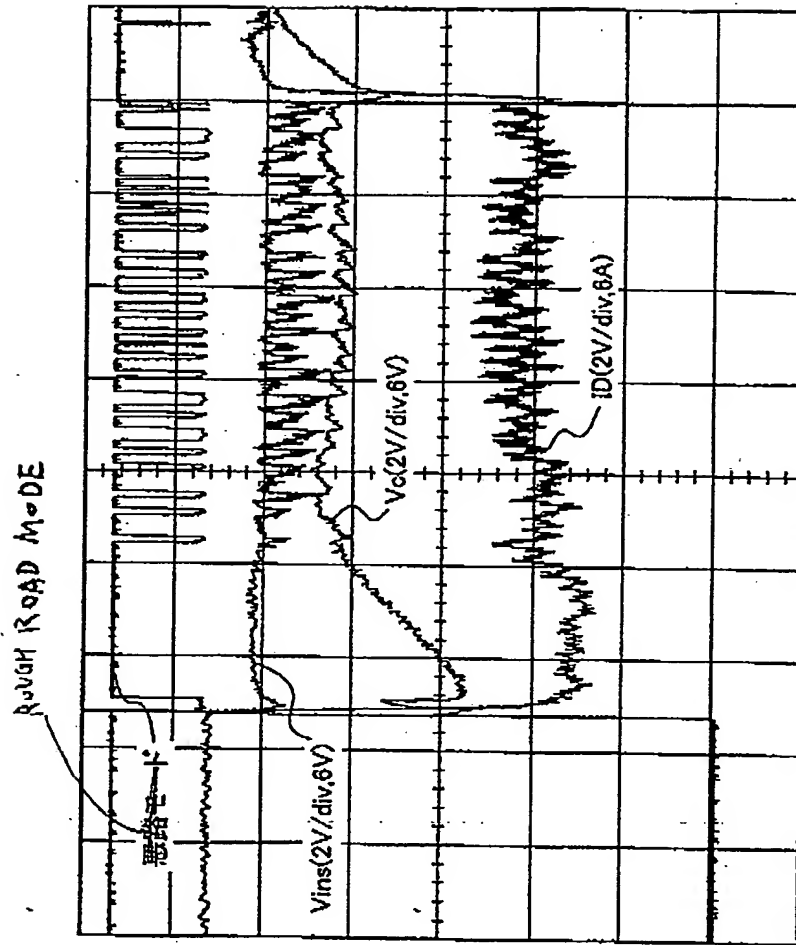


Fig. 6



The diagram illustrates two electronic circuits.
Circuit 100 (Current Limiting Circuit): This circuit is designed to limit current. It features a 5V supply connected to a network of resistors (R25, R205, R214, R200, R201, R202, R211, R210, R208, R212, R204, R207, R206, R213, R203, R27, R24, R23, R20, R6) and capacitors (C1, C13, C11, C15, C12). It includes two operational amplifiers (AMP1, AMP2), two comparators (P1, P2), and several transistors (T200, T201, T202, T203, T204, T205, T206, T21, T22). A diode D104 is also present. The output is labeled 'CURRENT LIMITING CIRCUIT' and '電流制限回路'.
Circuit 31 (Rough Read Mode Signal Circuit): This circuit generates a 'ROUGH READ MODE SIGNAL'. It includes a 5V supply, resistors (R205, R214, R200, R201, R202, R211, R210, R208, R212, R204, R207, R206, R213, R203, R27, R24, R23, R20, R6), capacitors (C1, C13, C11, C15, C12), and transistors (T200, T201, T202, T203, T204, T205, T206, T21, T22). It also features a diode D104 and a signal input labeled '信号' (Signal). The output is labeled 'ROUGH READ MODE SIGNAL' and '信号'.